LISTING OF CLAIMS

1. (Currently amended) An apparatus for controlling the system supply voltage in a system utilizing a spread spectrum clock signal, the apparatus including:

(a) a spread spectrum clock source having a modulation input;

(ab) a modulating arrangement including a modulation signal source having an output connected to the modulation input of the spread spectrum clock source, the modulating arrangement applying a first modulation to ~~operatively connected to apply a first modulation to one of the system supply voltage or~~ a clock signal frequency for the system, the first modulation varying the ~~one of the system supply voltage or~~ clock signal frequency about a nominal value for the ~~one of the system supply voltage or~~ clock signal frequency; ~~and~~

(bc) a corresponding modulating arrangement operatively connected to apply a corresponding modulation to ~~the other one of the~~ a system supply voltage ~~or the clock signal frequency~~, the corresponding modulation varying the ~~other one of the~~ system supply voltage ~~or the clock signal frequency~~ about a nominal value for ~~the other one of~~ the system supply voltage ~~or the clock signal frequency~~.

2-6 Canceled

7. (Currently Amended) The apparatus of Claim [[6]] 1 further including a power supply circuit having a reference input, and wherein the modulation signal source output is applied to modulate ~~the~~ a signal at the reference input.

8. (Previously presented) The apparatus of Claim 7 further including a summing junction connected to sum a DC reference voltage and the modulation signal source output to produce a summed output and apply the summed output to the reference input of the power supply circuit.

9. (Original) The apparatus of Claim 1 wherein the first modulation and the corresponding modulation comprise unequal waveforms.

10. (Currently amended) A spread spectrum clock system including:

(a) a spread spectrum clock source having a frequency modulation input and providing a clock signal;

(b) a power supply circuit providing a supply voltage output;

(c) a modulating arrangement including a modulator connected to provide a modulated reference input to the power supply circuit, the modulating arrangement applying ~~operatively connected to apply~~ a first modulation to ~~one of~~ the supply voltage output ~~or the frequency of the clock signal~~, the first modulation varying the ~~one of the~~ supply voltage output ~~or the frequency of the clock signal~~

about a nominal value for the ~~one of the~~ supply voltage output ~~or the frequency of the clock signal~~; and

(d) a corresponding modulating arrangement operatively connected to apply the supply voltage output to produce a corresponding modulation ~~to~~ in the ~~other one of the supply voltage output or the~~ frequency of the clock signal, the corresponding modulation varying the ~~other one of the supply voltage output or the~~ frequency of the clock signal about a nominal value for the ~~other one of the supply voltage output or the~~ frequency of the clock signal.

11. Canceled

12. (Currently Amended) The apparatus of Claim ~~11~~ 10 further including:

(a) a signal translator connected to receive the ~~system~~ supply voltage output and provide a translated output to the frequency modulation input of the spread spectrum clock source.

13-15 Canceled

16. (Original) The apparatus of Claim 10 wherein the first modulation waveform and the corresponding modulation waveform are unequal.

17. (Currently amended) A method for providing a spread spectrum clock signal for a circuit, the method including the steps of:

(a) modulating a power supply signal for the circuit at a first modulation to vary the power supply signal about a nominal supply voltage; ~~and~~

(b) conditioning the modulated power supply signal for the circuit to produce a conditioned signal at the first modulation;

(bc) applying the conditioned signal to a modulation input of a spread spectrum clock source circuit to modulate ~~modulating~~ the frequency of the clock signal for the circuit at a corresponding modulation to vary the frequency of the clock signal about a nominal clock signal frequency.

18. (Previously presented) The method of Claim 17 wherein the step of modulating the power supply signal for the circuit includes the step of:

(a) modulating a reference voltage input to a power supply for the circuit.

19-22 Canceled

23. (Original) The method of Claim 17 wherein the first modulation waveform and the corresponding modulation waveform are unequal.